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07/06/2004

MARSHALL, GERSTEIN & BORUN LLP
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233 S. WACKER DRIVE
CHICAGO, IL 60606

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Gregory E. Stanton	(Depositor's name)
<i>[Signature]</i>	(Signature)
October 1, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/614,229	07/07/2003	Ki Seog Kim	29936/39458	6155

TITLE OF INVENTION: ROW DECODER IN FLASH MEMORY AND ERASE METHOD OF FLASH MEMORY CELL USING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1370	\$300	\$1670	10/06/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LAM, DAVID	2818	365-185230

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 Marshall,
 2 Gerstein &
 3 Borun LLP

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(A) NAME OF ASSIGNEE

HYNIX SEMICONDUCTOR INC.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kyungki-Do, Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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Gregory E. Stanton, Reg.No. 45,127
 (Authorized Signature) (Date)

Oct. 1, 2004

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10/06/2004 MAHME2 00000006 10614229

01 FC:1501	1370.00 OP
02 FC:1504	300.00 OP
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